



IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Yih-Feng Chyan
John Michael Hergenrother
Donald Paul Monroe

CASE 15-6-9

Serial No. 09/648164

Group Art Unit 2826

Filed August 25, 2000

Examiner T.L. Dickey

Title Architecture For Circuit Connection Of A Vertical Transistor

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Enclosed is an amendment in the above-identified application.

Please charge the amount of \$168 to **Agere Systems Deposit Account No. 501735** to cover the fee as calculated below. Duplicate copies of this letter are enclosed.

CLAIMS AS AMENDED						
	Claims Remaining After Amendment		Highest No. Previously Paid For	Present Extra	Rate	Additional Fee
Total Claims for Fee Purposes	31	minus	31	0	x \$18 =	\$0
Independent Claims	7	minus	5	2	x \$84 =	\$168
Multiple Dependent Claims first presented with this amendment, if applicable					+ \$280	\$0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:						\$168

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 501735** as required to correct the error.

Respectfully,

Ferdinand M. Romano
Ferdinand M. Romano
Reg. No. 32752
407-371-3250

Date: 13 February 2003

Agere Systems Inc.
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Berkeley Heights, NJ 07922-2747

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks Washington, D.C. 20231 on 2-13-03.

Mary V. Carter
Printed name of person mailing paper or fee

[Signature]
Signature of person mailing paper or fee

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FEB 25 2003
TECHNOLOGY CENTER 2800